

Voltage Level Translator 24-Pin WQFN EP T/R

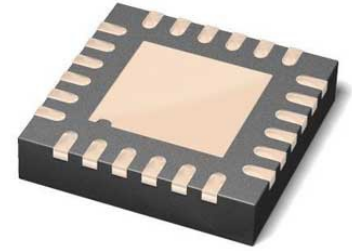
Manufacturer: [Texas Instruments, Inc](#)

Package/Case: QFN24

Product Type: Logic ICs

RoHS: RoHS Compliant/Lead free 

Lifecycle: Active



Images are for reference only

[Inquiry](#)

General Description

The TXS02612 is designed to interface the cell phone baseband with external SDIO peripherals. The device includes a 6-channel SPDT switch with voltage-level translation capability. This allows a single SDIO port to be interfaced with two SDIO peripherals. The TXS02612 has three separate supply rails that operate over the full range of 1.1 V to 3.6 V. This allows the baseband and SDIO peripherals to operate at different supply voltages if required.

The select (SEL) input is used to choose between the B0 port and B1 port. When SEL = Low, B0 port is selected; when SEL = High, B1 port is selected. SEL is referenced to VCCA. For the unselected B port, the clock output is held low, whereas the data and command I/Os are pulled high to their respective VCCB through a 70-k resistor ($\pm 30\%$ tolerance).

Key Features

6-to-12 Demultiplexer/Multiplexer Allows SDIO Port Expansion

Built-in Level Translator Eliminates Voltage Mismatch Between Baseband and SD Card or SDIO Peripheral

VCCA, VCCB0, and VCCB1 Each Operate Over Full 1.1-V to 3.6-V Range

Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II

ESD Performance A Port
2000-V Human-Body Model (A114-B)

100-V Machine Model (A115-A)

1500-V Charged-Device Model (C101)

±8-kV Contact Discharge IEC 61000-4-2 ESD Performance (B Port)

MicroStar Junior is a trademark of Texas Instruments.

Description

The TXS02612 is designed to interface the cell phone baseband with external SDIO peripherals. The device includes a 6-channel SPDT switch with voltage-level translation capability. This allows a single SDIO port to be interfaced with two SDIO peripherals. The TXS02612 has three separate supply rails that operate over the full range of 1.1 V to 3.6 V. This allows the baseband and SDIO peripherals to operate at different supply voltages if required.

The select (SEL) input is used to choose between the B0 port and B1 port. When SEL = Low, B0 port is selected; when SEL = High, B1 port is selected. SEL is referenced to VCCA. For the unselected B port, the clock output is held low, whereas the data and command I/Os are pulled high to their respective VCCB through a 70-k resistor (±30% tolerance).

Recommended For You

DS90LV049HMIX/NOPB

Texas Instruments, Inc

TSSOP-16

SCANSTA111MIX/NOPB

Texas Instruments, Inc

TSSOP-48

DS90LV049QMIX/NOPB

Texas Instruments, Inc

TSSOP-16

DS90CF384AQMIX/NOPB

Texas Instruments, Inc

TSSOP-56

DS90CR286AQMIX/NOPB

Texas Instruments, Inc

TSSOP-56

SN65LV1224BDBR

Texas Instruments, Inc

SSOP28

TCA9534PWR

Texas Instruments, Inc

TSSOP16

SN75173N

Texas Instruments, Inc

DIP

SN65LBC179D

Texas Instruments, Inc

SOP8

AM26LS31CD

Texas Instruments, Inc

SOP16

TFP401AMPZPEP

Texas Instruments, Inc

HTQFP100

SN75176AD

Texas Instruments, Inc

SOP-8

SN65LVDS3486D

Texas Instruments, Inc

SOP-16

ISO7221BDR

Texas Instruments, Inc

SOP8

SN65HVD33MDREP

Texas Instruments, Inc

SOP-14