

**NOR Flash Serial (SPI, Dual SPI, Quad SPI) 3V/3.3V 32M-bit  
32M/16M/8M x 1/2-bit/4-bit 8-Pin SOIJ T/R**



Images are for reference only

**Manufacturer:** [Microchip Technology, Inc](#)

**Package/Case:** SOP8

**Product Type:** Memory

**RoHS:** RoHS Compliant/Lead free 

**Lifecycle:** Active

[Inquiry](#)

## General Description

The SST26VF032B Serial Quad I/O (SQI) flash device utilizes a 4-bit multiplexed I/O serial interface to boost performance while maintaining the compact form factor of standard serial flash devices. SST26VF032B also support full command-set compatibility to traditional Serial Peripheral Interface (SPI) protocol. Operating at frequencies reaching 104 MHz, the SST26VF032B enables minimum latency execute-in-place (XIP) capability without the need for code shadowing on an SRAM. The device's high performance and reliability make it the ideal choice for Network Appliance, DSL and Cable Modems, Wireless Lan, Computing, Digital TV, Smart Meter, Server, Set Top Box, Automotive and other Industrial applications. Further benefits are achieved with SST's proprietary, high-performance CMOS SuperFlash® technology, which significantly improves performance and reliability, and lowers power consumption for high bandwidth, compact designs.

The **SST26VF032B** default at power up is with **WP# and HOLD pins enable and SIO2 and SIO3 pins disable** allowing for SPI protocol operations without register configuration.

The **SST26VF032BA** default at power up with **WP# and HOLD pins disable and SIO2 and SIO3 pins enable** allowing for Quad I/O operations without register configuration.

## Key Features

Serial Interface Architecture- Nibble-wide multiplexed I/O's with SPI-like serial command structure

Mode 0 and Mode 3,

x1/x2/x4 Serial Peripheral Interface (SPI) Protocol and SQI protocol

Burst Modes- Continuous linear burst, 8/16/32/64 Byte linear burst with wrap-around

Page-Program- 256 Bytes per page in x1 or x4 mode

Flexible Erase Capability- Uniform 4 KByte sectors, Four 8 KByte top and bottom parameter overlay blocks, One 32 KByte top and bottom overlay block, Uniform 64 KByte overlay blocks

Software Write Protection- Individual Block-Locking: 64 KByte blocks, two 32 KByte blocks, and eight 8 KByte parameter blocks

Low Power Consumption: Active Read current: 15 mA (typical @ 104 MHz), Standby Current: 15  $\mu$ A (typical)

SFDP (Serial Flash Discoverable Parameters)

Temperature options:

Industrial -40°C to +85°C and Extended -40°C to +105°C

Packages Available: 8-contact WDFN (6mm x 5mm), 8-lead SOIC (208 mil), 24-ball TBGA (6mm x 8mm)

All devices are RoHS compliant

Automotive Grade 2 and 3 available

## Recommended For You

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### **SST39VF1602-70-4I-EKE**

Microchip Technology, Inc

TSOP48

### **SST25VF080B-50-4C-S2AF**

Microchip Technology, Inc

SOP8

### **AT24C64D-SSHMT**

Microchip Technology, Inc

SOP8

### **AT25256B-SSHL-T**

Microchip Technology, Inc

SOP8

### **AT24C08C-SSHMT**

Microchip Technology, Inc

SOP8

### **AT24C256C-SSHL-T**

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SOP8

### **AT24C32D-SSHMT**

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### **AT24C02C-SSHMT**

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SOP8

### **AT24C16C-SSHMT**

Microchip Technology, Inc

SOP-8

### **AT93C56B-SSHMT**

Microchip Technology, Inc

SOP8

### **AT25040B-SSHL-B**

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SOP-8

### **AT25128B-SSPDGV-T**

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**SST39SF040-70-4C-WHE**

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**SST39SF010A-70-4C-NHE**

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PLCC32

**SST26VF016-80-5I-S2AE**

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