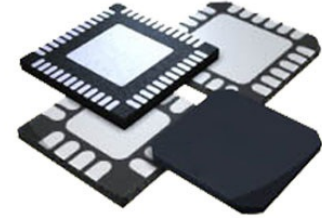


Divider -40°C to 85°C 16-Pin QFN EP T/R



Images are for reference only

[Inquiry](#)

Manufacturer: [Analog Devices, Inc](#)

Package/Case: QFN

Product Type: Logic ICs

RoHS: RoHS Compliant/Lead free 

Lifecycle: Obsolete

General Description

The HMC794LP3E is a SiGe BiCMOS low noise programmable frequency divider in a 3x3mm leadless surface mount package. The circuit can be programmed to divide from $N = 1$ to $N = 4$ in the 200 MHz to 2 GHz input frequency range. The high level output power (up to 10 dBm) with a very low SSB phase noise and 50% duty cycle makes this device ideal for low noise clock generation, LO generation and LO drive applications. Configurable bias controls allow power minimization of up to 20%.

Key Features

Low Noise Floor:-163 dBc/Hz at 10 MHz offset &-160 dBc/Hz at 100 kHz offset

Programmable Frequency Divider, $N = 1, 2, 3$ or 4

200 MHz to 2 GHz Input Frequency Range

50% Duty Cycle Outputs

Up to +10 dBm Output Power

Sleep Mode: Consumes $<1 \mu\text{A}$

16 Lead 3x3mm SMT Package: 9mm²

Application

LO Generation with Low Noise Floor

Clock Generators

Mixer LO Drive

Military Applications

Test Equipment

Sensors

Recommended For You

HMC624ALP4E

Analog Devices, Inc

QFN24

HMC952ALP5GE

Analog Devices, Inc

QFN

HMC361S8GE

Analog Devices, Inc

SOP-8

HMC253AQS24E

Analog Devices, Inc

QFN

HMC346MS8G

Analog Devices, Inc

MSOP8

HMC1119LP4ME

Analog Devices, Inc

QFN

HMC659LC5

Analog Devices, Inc

QFN

HMC909LP4E

Analog Devices, Inc

QFN

HMC564LC4

Analog Devices, Inc

QFN

HMC1021LP4E

Analog Devices, Inc

QFN

HMC241AQS16E

Analog Devices, Inc

SSOP16

HMC424LP3E

Analog Devices, Inc

QFN

HMC662LP3E

Analog Devices, Inc

QFN

HMC8038LP4CE

Analog Devices, Inc

QFN16

HMC363S8G

Analog Devices, Inc

SOP8