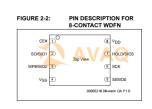


NOR Flash Serial (SPI, Dual SPI, Quad SPI) 3V/3.3V 32M-bit 32M/16M/8M x 1/2-bit/4-bit 8-Pin WDFN EP Tube

Manufacturer:	Microchip Technology, Inc
Package/Case:	WDFN-8
Product Type:	Memory
RoHS:	RoHS Compliant/Lead free WHS
Lifecycle:	Active



Images are for reference only

Inquiry

General Description

The SST26VF032B Serial Quad I/O (SQI) flash device utilizes a 4-bit multiplexed I/O serial interface to boost performance while maintaining the compact form factor of standard serial flash devices. SST26VF032B also support full command-set compatibility to traditional Serial Peripheral Interface (SPI) protocol. Operating at frequencies reaching 104 MHz, the SST26VF032B enables minimum latency execute-in-place (XIP) capability without the need for code shadowing on an SRAM. The device's high performance and reliability make it the ideal choice for Network Appliance, DSL and Cable Moderns, Wireless Lan, Computing, Digital TV, Smart Meter, Server, Set Top Box, Automotive and other Industrial applications. Further benefits are achieved with SST's proprietary, high-performance CMOS SuperFlash® technology, which significantly improves performance and reliability, and lowers power consumption for high bandwidth, compact designs.

The **SST26VF032B** default at power up is with **WP# and HOLD pins enable and SIO2 and SIO3 pins disable** allowing for SPI protocol operations without register configuration.

The SST26VF032BA default at power up with WP# and HOLD pins disable and SIO2 and SIO3 pins enable allowing for Quad I/O operations without register configuration.

Key Features

Application

Serial interface architecture - mode 0/3

Computers & Computer Peripherals, Industrial, Communications & Networking, Consumer Electronics

Nibble-wide multiplexed I/O with SPI-like serial command structure

High speed clock frequency - 104MHz maximum

Burst modes - continuous linear burst

Superior reliability

Endurance - 100000 cycles minimum

Greater than 100 years data retention

Low power consumption

Fast erase time

End-of-write detection - software polling the BUSY bit in status register

Flexible erase capability

Write-suspend - suspend program or erase operation to access another block/sector

Software reset (RST) mode

Software write protection

Individual-block write protection with permanent lock-down capability

Read protection on top and bottom 8-Kbyte parameter blocks

Security ID

User-programmable area

FIGURE 2-2: PIN DESCRIPTION FOR 8-CONTACT WDFN

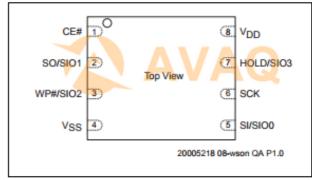
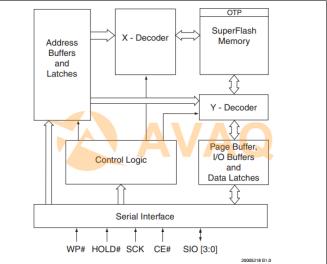


FIGURE 1-1: FUNCTIONAL BLOCK DIAGRAM



Recommended For You

SST39VF1602-70-4I-EKE

Microchip Technology, Inc TSOP48

AT25256B-SSHL-T Microchip Technology, Inc SOP8

AT24C32D-SSHM-T Microchip Technology, Inc SOP8

AT93C56B-SSHM-T Microchip Technology, Inc SOP8

SST39SF040-70-4C-WHE

Microchip Technology, Inc TSOP SST25VF080B-50-4C-S2AF

Microchip Technology, Inc SOP8

AT24C08C-SSHM-T Microchip Technology, Inc SOP8

AT24C02C-SSHM-T Microchip Technology, Inc SOP8

AT25040B-SSHL-B Microchip Technology, Inc SOP-8

SST39SF010A-70-4C-NHE Microchip Technology, Inc PLCC32 AT24C64D-SSHM-T

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AT24C256C-SSHL-T Microchip Technology, Inc SOP8

AT24C16C-SSHM-B Microchip Technology, Inc SOP-8

AT25128B-SSPDGV-T Microchip Technology, Inc SOP8

Microchip Technology, Inc SOP8

SST26VF016-80-5I-S2AE