

## FPGA Configuration PROM Parallel/Serial 1M-bit 2.5V/3.3V 20-Pin SOIC



Images are for reference only

[Inquiry](#)

<b>Manufacturer:</b>	<a href="#">AMD Xilinx, Inc</a>
<b>Package/Case:</b>	SOP20
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

### General Description

Xilinx introduces the XC18V00 series of in-system programmable configuration PROMs (Figure 1). Devices in this 3.3V family include a 4-megabit, a 2-megabit, a 1-megabit, and a 512-kilobit PROM that provide an easy-to-use, cost-effective method for reprogramming and storing Xilinx FPGA configuration bitstreams.

When the FPGA is in Master Serial mode, it generates a configuration clock that drives the PROM. A short access time after CE and OE are enabled, data is available on the PROM DATA (D0) pin that is connected to the FPGA DIN pin. New data is available a short access time after each rising clock edge. The FPGA generates the appropriate number of clock pulses to complete the configuration. When the FPGA is in Slave Serial mode, the PROM and the FPGA are clocked by an external clock.

When the FPGA is in Master SelectMAP mode, the FPGA generates a configuration clock that drives the PROM. When the FPGA is in Slave Parallel or Slave SelectMAP mode, an external oscillator generates the configuration clock that drives the PROM and the FPGA. After CE and OE are enabled, data is available on the PROM's DATA (D0-D7) pins. New data is available a short access time after each rising clock edge. The data is clocked into the FPGA on the following rising edge of the CCLK. A free-running oscillator can be used in the Slave Parallel or Slave SelectMAP modes.

Multiple devices can be cascaded by using the CEO output to drive the CE input of the following device. The clock inputs and the DATA outputs of all PROMs in this chain are interconnected. All devices are compatible and can be cascaded with other members of the family or with the XC17V00 one-time programmable serial PROM family.

### Recommended For You

#### [XCF128XFT64C](#)

AMD Xilinx, Inc  
BGA

#### [XC18V04VQ44I](#)

AMD Xilinx, Inc  
QFP

#### [XC17128EPD8I](#)

AMD Xilinx, Inc  
DIP8

#### [XC1765ELSO8C](#)

AMD Xilinx, Inc  
SOP8

#### [XC18V04VQ44C](#)

AMD Xilinx, Inc  
QFP44

#### [XC18V04VQG44C](#)

AMD Xilinx, Inc  
QFP

**XCF32PVOG48C**

AMD Xilinx, Inc

TSOP48

**XC18V01PCG20C**

AMD Xilinx, Inc

PLCC20

**XCF04SVO20C**

AMD Xilinx, Inc

TSSOP20

**XC2C256-7CPG132I**

AMD Xilinx, Inc

BGA132

**XCF04SVOG20C**

AMD Xilinx, Inc

TSSOP20

**XCF08PFS48C**

AMD Xilinx, Inc

BGA

**XC18V01VQ44C**

AMD Xilinx, Inc

TQFP44

**XC1765EPD8C**

AMD Xilinx, Inc

DIP8

**XC18V02VQG44C**

AMD Xilinx, Inc

QFP44