

PROM Serial 1M-bit 3.3V 8-Pin PDIP



Images are for reference only

[Inquiry](#)

Manufacturer: [AMD Xilinx, Inc](#)

Package/Case: DIP8

Product Type: Memory

Lifecycle: Obsolete

General Description

XC17S100APD8C is a type of Field-Programmable Gate Array (FPGA) manufactured by Xilinx.

Key Features

It has a capacity of 1,008 configurable logic blocks.

It has a maximum clock frequency of 100 MHz.

It has 8 input/output (I/O) pins.

It operates with a voltage range of 3.0V to 3.6V.

It comes in a DIP package.

Application

Industrial automation

Robotics

Communications equipment

Medical equipment

Test and measurement equipment

Recommended For You

XC17S20XLPD8C

AMD Xilinx, Inc

DIP8

XC17S30XLPD8I

AMD Xilinx, Inc

DIP8

XC17S40XLPD8C

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DIP8

XC17S30XLPDG8C

AMD Xilinx, Inc

DIP8

XC17S05PD8I

AMD Xilinx, Inc

DIP8

XC17S40PD8C

AMD Xilinx, Inc

DIP8

XC17S30XLPD8C

AMD Xilinx, Inc

DIP8

XC17V16VQ44I

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VQFP44

XC17S50ASO20C

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SOP-20L

XC17S30XLVOG8I

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8-TSOP

XC17S40SO20I

AMD Xilinx, Inc

20-SOIC

XC17S20VO8I

AMD Xilinx, Inc

8-TSOP

XC17S10XLVO8I

AMD Xilinx, Inc

8-TSOP

XC17S20XLVO8C

AMD Xilinx, Inc

8-TSOP

XC17S40XLPD8I

AMD Xilinx, Inc

DIP8