# LAN91C111I-NU

# Ethernet CTLR Single Chip 10Mbps/100Mbps 3.3V 128-Pin TQFP Tray

Manufacturer: Microchip Technology, Inc

Package/Case: TQFP128

**Product Type:** Communication & Networking ICs

RoHS: RoHS Compliant/Lead free

**Lifecycle:** Active



Images are for reference only

Inquiry

#### **General Description**

The Microchip LAN91C111 is designed to facilitate the implementation of a third generation of Fast Ethernet connectivity solutions for embedded applications. For this third generation of products, flexibility and integration dominate the design requirements. The LAN91C111 is a mixed signal Analog/Digital device that implements the MAC and PHY portion of the CSMA/CD protocol at 10 and 100 Mbps. The design will also minimize data throughput constraints utilizing a 32-bit, 16-bit or 8-bit bus Host interface in embedded applications.

The total internal memory FIFO buffer size is 8 Kbytes, which is the total chip storage for transmit and receive operations.

The Microchip LAN91C111 is software compatible with the LAN9000 family of products.

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For product comparison, please consider:LAN9218

#### **Key Features**

Fully supports full duplex switched Ethernet

Supports burst data transfer 8kbyte Internal memory for receive and transmit FIFO buffers

Enhanced power management

Built-in transparent arbitration for slave sequential access architecture

Flat MMU architecture with symmetric transmit and receive structures and queues

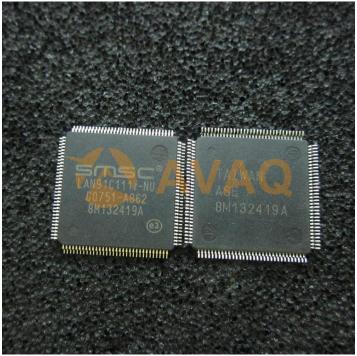
Low power CMOS design

MII Management serial interface

Adaptive equalizer

Baseline wander correction





#### **Recommended For You**

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Microchip Technology, Inc

QFP

#### LAN91C96-MU

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TQFP100

#### LAN91C111-NC

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#### LAN91C111I-NS

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QFP128

#### LAN91C93I-MU

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QFP

### LAN91C111i-NC

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QFP

# LAN9252I/ML

Microchip Technology, Inc

QFN-64

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#### **LAN9420I-NU**

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# **LAN9215-MT**

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QFP100

#### **LAN9118-MT**

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TQFP64

LAN9221I-ABZJ

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QFN56

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