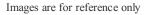


GAL16V8D-10LJ

SPLD GAL Family 8 Macro Cells 71.4MHz 5V 20-Pin PLCC

Manufacturer:	Lattice Semiconductor Corp
Package/Case:	PLCC
Product Type:	Programmable Logic ICs
Lifecycle:	Obsolete





Inquiry

General Description

The GAL16V8, at 3.5 ns maximum propagation delay time, combines a high performance CMOS process with Electrically Erasable (E2) floating gate technology to provide the highest speed performance available in the PLD market. High speed erase times (<100ms) allow the devices to be reprogrammed quickly and efficiently.

The generic architecture provides maximum design flexibility by allowing the Output Logic Macrocell (OLMC) to be configured by the user. An important subset of the many architecture configurations possible with the GAL16V8 are the PAL architectures listed in the table of the macrocell description section. GAL16V8 devices are capable of emulating any of these PAL architectures with full function/fuse map/parametric compatibility.

Unique test circuitry and reprogrammable cells allow complete AC, DC, and functional testing during manufacture. As a result, Lattice Semiconductor delivers 100% field programmability and functionality of all GAL products. In addition, 100 erase/write cycles and data retention in excess of 20 years are specified.



Recommended For You

GAL16V8D-25LP Lattice Semiconductor Corp DIP20

GAL16V8D-10LP Lattice Semiconductor Corp DIP

GAL20V8B-15LP Lattice Semiconductor Corp DIP24

GAL16V8D-25QPI Lattice Semiconductor Corp DIP

GAL22V10D-10LJN Lattice Semiconductor Corp PLCC GAL16V8D-15QJ Lattice Semiconductor Corp PLCC20

GAL22V10D-15LJ Lattice Semiconductor Corp PLCC28

GAL16V8D-25LJN Lattice Semiconductor Corp PLCC

GAL20V8B-15LPN Lattice Semiconductor Corp DIP

GAL16V8D-25LJI Lattice Semiconductor Corp PLCC20 GAL16V8D-15LPN Lattice Semiconductor Corp DIP20

GAL16V8D-15LJN Lattice Semiconductor Corp PLCC20

GAL16V8D-10LPN Lattice Semiconductor Corp DIP

GAL18V10B-20LP Lattice Semiconductor Corp DIP20

Lattice Semiconductor Corp PLCC20

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