

## PROM Serial 243.3K-bit 3.3V 8-Pin PDIP



Images are for reference only

[Inquiry](#)

**Manufacturer:** [AMD Xilinx, Inc](#)

**Package/Case:** DIP8

**Product Type:** Memory

**Lifecycle:** Obsolete

### General Description

The XC17S30XLPD8I is a Complex Programmable Logic Device (CPLD) designed to implement custom digital logic functions. CPLDs are programmable logic devices that can be used to create digital circuits, perform specific functions, and replace traditional discrete logic components.

### Key Features

**Logic Capacity:** The XC17S30XLPD8I has a logic capacity of 30 macrocells. Macrocells are programmable building blocks used to implement custom logic functions.

**In-System Programmable:** The CPLD can be programmed or reprogrammed in-system, which means it can be configured after being soldered onto a circuit board.

**Low Power Consumption:** The device is designed with low power consumption features, making it suitable for power-sensitive applications.

**High-Speed Performance:** The CPLD offers high-speed operation, enabling efficient data processing and logic functions.

**Eight Input Pins:** The device has eight input pins, allowing it to receive external signals or data.

**Programmable Interconnect:** It includes a programmable interconnect structure that allows the designer to create custom connections between macrocells.

### Application

Digital Logic Design

System Integration

Prototyping

Communication

Systems

Industrial Control

Systems

### Recommended For You

#### XC17S20XLPD8C

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#### XC17S40XLPD8C

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#### XC17S30XLPDG8C

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**XC17S05PD8I**

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**XC17S100APD8C**

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**XC17S40PD8C**

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**XC17S30XLPD8C**

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**XC17V16VQ44I**

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VQFP44

**XC17S50ASO20C**

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SOP-20L

**XC17S30XLVOG8I**

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8-TSOP

**XC17S40SO20I**

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20-SOIC

**XC17S20VO8I**

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8-TSOP

**XC17S10XLVO8I**

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8-TSOP

**XC17S20XLVO8C**

AMD Xilinx, Inc

8-TSOP

**XC17S40XLPD8I**

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DIP8