

## Counter Dual 4-Bit Binary UP 16-Pin PDIP Tube

Manufacturer: <u>Texas Instruments, Inc</u>

Package/Case: DIP16

**Product Type:** Logic ICs

RoHS: RoHS Compliant/Lead free RoHS

Lifecycle: Active



Images are for reference only



## **General Description**

CD4518 Dual BCD Up-Counter and CD4520 Dual Binary Up-Counter each consist of two identical, internally synchronous4-stage counters. The counter stages are D-type flip-flops having interchangeable CLOCK and ENABLE lines for incrementingon either the positive-going or negative-going transition. For single-unit operation the ENABLE input is maintained highand the counter advances on each positive-going transition of the CLOCK. The counters are cleared by high levels on theirRESET lines.

The counter can be cascaded in the ripple mode by connecting Q4 to the enable input of the subsequent counter while the CLOCK input of the latter is held low.

The CD4518B and CD4520B types are supplied in 16-lead hermetic dual-in-line ceramic packages (F3A suffix), 16-lead dual-in-line plastic packages (E suffix), 16-lead small-outline packages (M, M96, and NSR suffixes), and 16-lead thin shrink small-outline packages (PW and PWR suffixes).

## **Key Features**

Medium-speed operation - 6-MHz typical clock frequency at 10 V

Positive- or negative-edge triggering

Synchronous internal carry propagation

100% tested for quiescent current at 20 V

Maximum input current of 1 µA at 18 V over full package-temperature range; 100 nA at 18 V and 25°C

Noise margin (over full package-temperature range):

1 V at VDD = 5 V

2 V at VDD = 10 V

2.5 V at VDD = 15 V

5-V, 10-V, and 15-V parametric ratings

Standardized, symmetrical output characteristics

Meets all requirements of JEDEC Tentative Standard No. 13B, "Standard Specifications for Description of 'B' Series CMOS Devices"

Applications

Multistage synchronous counting

Multistage ripple counting

Frequency dividers

Data sheet acquired from Harris Semiconductor.

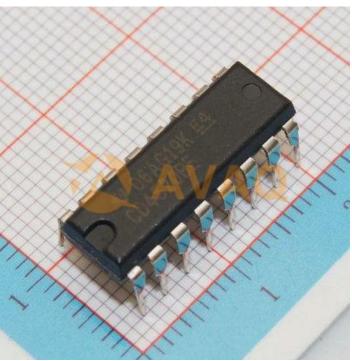
Description

CD4518 Dual BCD Up-Counter and CD4520 Dual Binary Up-Counter each consist of two identical, internally synchronous4-stage counters. The counter stages are D-type flip-flops having interchangeable CLOCK and ENABLE lines for incrementingon either the positive-going or negative-going transition. For single-unit operation the ENABLE input is maintained highand the counter advances on each positive-going transition of the CLOCK. The counters are cleared by high levels on theirRESET lines.

The counter can be cascaded in the ripple mode by connecting Q4 to the enable input of the subsequent counter while the CLOCK input of the latter is held low.

The CD4518B and CD4520B types are supplied in 16-lead hermetic dual-in-line ceramic packages (F3A suffix), 16-lead dual-in-line plastic packages (E suffix), 16-lead small-outline packages (M, M96, and NSR suffixes), and 16-lead thin shrink small-outline packages (PW and PWR suffixes).





## **Recommended For You**

**CD4017BE** 

Texas Instruments, Inc

DIP16

**CD74AC161M** 

Texas Instruments, Inc

SOP16

**CD4026BE** 

Texas Instruments, Inc

DIP

**CD4020BE** 

Texas Instruments, Inc

DIP16

CD4510BNSR

Texas Instruments, Inc

SOP16

**CD40193BE** 

Texas Instruments, Inc

DIP

**CD4060BM** 

Texas Instruments, Inc

SOP

**CD4516BE** 

Texas Instruments, Inc

DIP16

**CD40110BE** 

Texas Instruments, Inc

DIP

**CD4022BE** 

Texas Instruments, Inc

DIP

**CD4024BM** 

Texas Instruments, Inc

SOP14

**CD4040BE** 

Texas Instruments, Inc

DIP16

**CD4060BE** 

Texas Instruments, Inc

DIP16

CD74HCT193E

Texas Instruments, Inc

DIP

**CD74HC193E** 

Texas Instruments, Inc

DIP