


EEPROM Parallel 1M-bit 128K x 8 5V 32-Pin PLCC Tube

Manufacturer:	<u>Microchip Technology, Inc</u>
Package/Case:	PLCC-32
Product Type:	Memory
RoHS:	RoHS Compliant/Lead free 
Lifecycle:	Active



Images are for reference only

[Inquiry](#)

General Description

The Microchip AT28C010 is a high-performance 1-megabit Parallel EEPROM available in both Industrial and Military temperatures, offering access times to 120ns with power dissipation of 220mW (440mWmilitary). Deselected, CMOS standby current is less than 200 μ A (300 μ A military). Access like static RAM for the read or write cycle without external components; a 128byte page register to allow writing of up to 128bytes simultaneously. Features an internal Error Correction Circuit for extended endurance and improved data retention. Optional Software Data Protection mechanism guards against inadvertent writes; and an extra 128 bytes of EEPROM enables device identification or tracking.

The AT28C010 is also available DUAL MARKED where applicable with the appropriate Standard Military Drawing Number - 5962-382670xxMxx. See the separate MIL datasheet for exact availability.

Key Features

Features

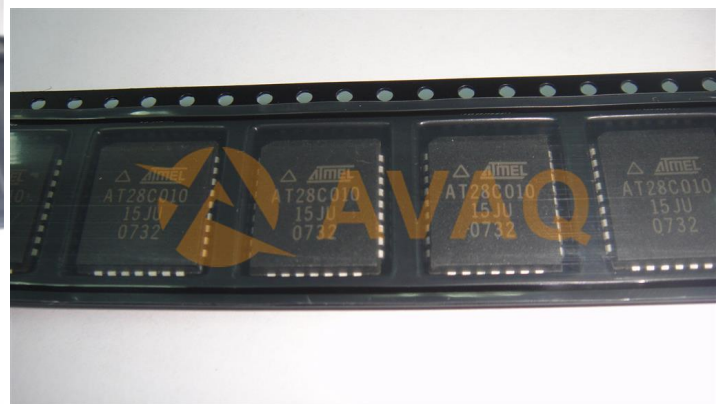
- Fast Read Access Time - 120 ns
- Automatic Page Write Operation
 - Internal Address and Data Latches for 128-Bytes
 - Internal Control Timer
- Fast Write Cycle Time
 - Page Write Cycle Time - 10 ms Maximum
 - 1 to 128-Byte Page Write Operation
- Low Power Dissipation
 - 80 mA Active Current
 - 300 μ A CMOS Standby Current
- Hardware and Software Data Protection
- DATA Polling for End of Write Detection
- High Reliability CMOS Technology
 - Endurance: 104 or 105 Cycles
 - Data Retention: 10 Years
- Single 5V \square 10% Supply
- CMOS and TTL Compatible Inputs and Outputs
- JEDEC Approved Byte-Wide Pinout

Description

The AT28C010 is a high-performance Electrically Erasable and Programmable Read Only Memory. Its one megabit of memory is organized as 131,072 words by 8 bits. Manufactured with Atmel's advanced nonvolatile CMOS technology, the device offers access times to 120 ns with power dissipation of just 440 mW. When the device is deselected, the CMOS standby current is less than 300uA.

The AT28C010 is accessed like a Static RAM for the read or write cycle without the need for external components. The device contains a 128-byte page register to allow writing of up to 128- bytes simultaneously. During a write cycle, the address and 1 to 128-bytes of data are internally latched, freeing the address and data bus for other operations. Following the initiation of a write cycle, the device will automatically write the latched data using an internal control timer. The end of a write cycle can be detected by DATA POLLING of I/O7. Once the end of a write cycle has been detected a new access for a read or write can begin.

Atmel's 28C010 has additional features to ensure high quality and manufacturability. The device utilizes internal error correction for extended endurance and improved data retention characteristics. An optional software data protection mechanism is available to guard against inadvertent writes. The device also includes an extra 128-bytes of EEPROM for device identification or tracking.



Recommended For You

AT93C46E-PU

Microchip Technology, Inc

DIP8

AT93C46D-PU

Microchip Technology, Inc

DIP8

AT24C64D-SSHMT

Microchip Technology, Inc

SOP8

AT24C128C-MAHMT

Microchip Technology, Inc
UDFN-8

AT93C66B-XHM-T

Microchip Technology, Inc
TSSOP8

AT25256B-SSHL-T

Microchip Technology, Inc
SOP8

AT24C08C-SSHM-T

Microchip Technology, Inc
SOP8

AT24C04C-PUM

Microchip Technology, Inc
DIP8

AT24C256C-SSHL-T

Microchip Technology, Inc
SOP8

AT24C02C-XHM-T

Microchip Technology, Inc
TSSOP8

AT24C02C-XHM-B

Microchip Technology, Inc
TSSOP8

AT24C32D-SSHM-T

Microchip Technology, Inc
SOP8

AT24C02C-SSHM-T

Microchip Technology, Inc
SOP8

AT24C16C-SSHM-B

Microchip Technology, Inc
SOP-8

AT93C56B-SSHM-T

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SOP8