

## GPS Receiver 1575.42MHz 32-Pin VFQFPN EP Tray

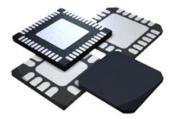
Manufacturer: STMicroelectronics, Inc

Package/Case: QFN

Product Type: Discrete Semiconductor Modules

RoHS: RoHS Compliant/Lead free

Lifecycle: Obsolete



Images are for reference only

Inquiry

## **General Description**

The STA5630 is a fully integrated RF front-end able to down-convert either the GPS L1 signal from 1575.42 MHz to 4.092 MHz.

The STA5630 embeds high performance LNA minimizing external component count. The chip uses state of the art CMOS 65 nm technology.

A 3-bit ADC converts the IF signal to Sign (SIGN) and Magnitude (MAG0 and MAG1). The magnitude bits are internally integrated in order to control the variable gain amplifiers. The VGA gain can be also set by the SPI interface.

The STA5630 accepts a range of reference clocks (10 to 52 MHz) and generates a 16.368 MHz sampling clock (GPS\_CLK) for the baseband. The STA5630 embeds LDO to supply the internal core of the device facilitating requirements to external power supply.

High performance, low power, and cost effective device, the STA5630 is the ideal solution for automotive, cellular and consumer battery powered applications.

## **Key Features**

Integrated LNA

Low power consumption (< 25 mW)

1.8 V supply voltage

GPS and Galileo compliant

Minimum external components

Serial interface

3 bits A/D converter

CMOS 65 nm technology

Standard QFN-32 package

Ambient temperature range: -40 °C to +85 °C

## **Recommended For You**

STA5620 ST25RU3993-BQFT ST25R95-VMD5T

STMicroelectronics, Inc STMicroelectronics, Inc STMicroelectronics, Inc

QFN QFN48 QFN32

STA8090FG STA8088GA ST95HF-VMD5T

STMicroelectronics, Inc STMicroelectronics, Inc STMicroelectronics, Inc

BGA QFN QFN32

ST25DV16K-JFR6D3 ST25R3920-AQWT ST25DV04K-IFR6C3

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