

Zero Delay Buffer 8-Out HCSL Differential 48-Pin TSSOP Tube



Images are for reference only

[Inquiry](#)

Manufacturer: [Renesas Technology Corp](#)

Package/Case: TSSOP48

Product Type: Clock & Timer ICs

RoHS: RoHS Compliant/Lead free 

Lifecycle: Active

General Description

The 9DB833 zero-delay buffer supports PCIe Gen3 requirements, while being backwards compatible to PCIe Gen2 and Gen1. The 9DB833 is driven by a differential SRC output pair from an IDT 932S421 or 932SQ420 or equivalent main clock generator.

Key Features

Eight 0.7V HCSL differential output pairs

Phase jitter: PCIe Gen3 < 1ps rms

Phase jitter: PCIe Gen2 < 3.1ps rms

Phase jitter: PCIe Gen1 < 86ps peak to peak

Supports zero delay buffer mode and fanout mode

Bandwidth programming available

3 selectable SMBus Addresses

50-110 MHz operation in PLL mode

5-166 MHz operation in Bypass mode

Recommended For You

9DB433AGLFT

Renesas Technology Corp

TSSOP-28

9DB803DGILFT

Renesas Technology Corp

TSSOP-48

9DB102BGLF

Renesas Technology Corp

TSSOP-20

9DBV0831AKILF

Renesas Technology Corp
QFN

9DBV0941AKLF

Renesas Technology Corp
VFQFPN48

9DB633AGLFT

Renesas Technology Corp
TSSOP-28

9DB403DGLFT

Renesas Technology Corp
TSSOP28

9DB423BGLF

Renesas Technology Corp
28-TSSOP

9DB102BFLF

Renesas Technology Corp
SSOP

9DB833AGLF

Renesas Technology Corp
TSSOP48

9DBV0841AKLF

Renesas Technology Corp
QFN

9DBL411BGILF

Renesas Technology Corp
20-TSSOP

9DBU0641AKILF

Renesas Technology Corp
VFQFPN-40

9DBU0241AKLF

Renesas Technology Corp
VFQFPN-24

9DBU0431AKLF

Renesas Technology Corp
VFQFPN-32