
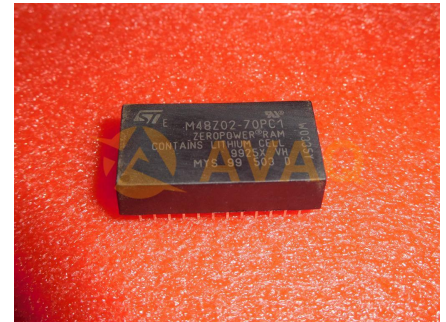


NVRAM NVSRAM Parallel 16Kbit 5V 24-Pin PCDIP Tube

Manufacturer:	<u>STMicroelectronics, Inc</u>
Package/Case:	DIP
Product Type:	Memory
RoHS:	RoHS Compliant/Lead free 
Lifecycle:	Active



Images are for reference only

[Inquiry](#)

General Description

The M48Z02/12 ZEROPOWER RAM is a 2 K x 8 non-volatile static RAM which is pin and function compatible with the DS1220. A special 24-pin, 600 mil DIP CAPHAT package houses the M48Z02/12 silicon with a long-life lithium button cell to form a highly integrated battery-backed memory solution. The M48Z02/12 button cell has sufficient capacity and storage life to maintain data functionality for an accumulated time period of at least 10 years in the absence of power over commercial operating temperature range. The M48Z02/12 is a non-volatile pin and function equivalent to any JEDEC standard 2 K x 8 SRAM. It also easily fits into many ROM, EPROM, and EEPROM sockets, providing the non-volatility of PROMs without any requirement for special WRITE timing or limitations on the number of WRITES that can be performed.

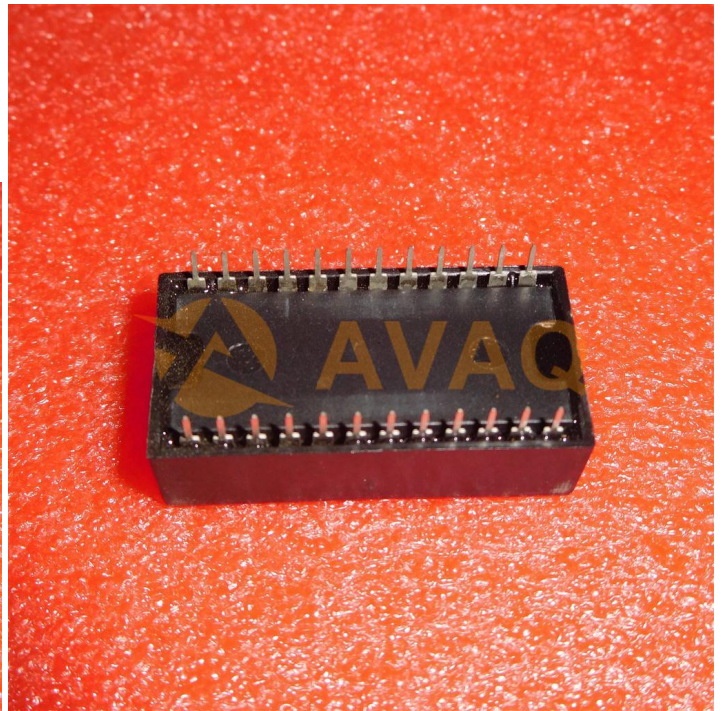
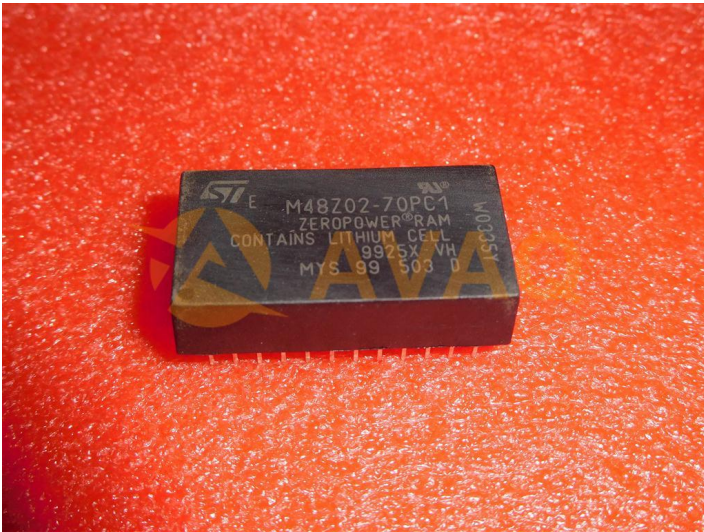
Key Features

Integrated, ultra low power SRAM and power-fail control circuit

Unlimited WRITE cycles

READ cycle time equals WRITE cycle time

Automatic power-fail chip deselect and WRITE protection



Recommended For You

M48Z35-70PC1

STMicroelectronics, Inc

DIP

M48Z08-100PC1

STMicroelectronics, Inc

DIP

M48Z12-150PC1

STMicroelectronics, Inc

DIP

M48Z02-150PC1

STMicroelectronics, Inc

DIP

M48Z58Y-70PC1

STMicroelectronics, Inc

DIP28

M48Z129V-85PM1

STMicroelectronics, Inc

DIP

M48T35AV-10PC1

STMicroelectronics, Inc

DIP

M48T35AV-10MH1F

STMicroelectronics, Inc

SOP28

M48Z35Y-70PC1

STMicroelectronics, Inc

DIP

M48Z58-70PC1

STMicroelectronics, Inc

DIP

M48Z12-70PC1

STMicroelectronics, Inc

DIP

M48Z58Y-70MH1F

STMicroelectronics, Inc

SOH-28

M48Z32V-35MH1

STMicroelectronics, Inc

SSOP44

M48Z35Y-70MH1E

STMicroelectronics, Inc

N

M48Z32V-35MH1TR

STMicroelectronics, Inc

SSOP44