

FPGA FLEX 10K Family 50K Gates 2880 Cells 125MHz 0.42um  
Technology 3.3V 240-Pin RQFP



Images are for reference only

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

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## General Description

Altera's FLEX 10K devices are the industry's first embedded PLDs. Based on reconfigurable CMOS SRAM elements, the Flexible Logic Element MatriX (FLEX) architecture incorporates all features necessary to implement common gate array megafunctions. With up to 250,000 gates, the FLEX 10K family provides the density, speed, and features to integrate entire systems, including multiple 32-bit buses, into a single device.

FLEX 10K devices are reconfigurable, which allows 100% testing prior to shipment. As a result, the designer is not required to generate test vectors for fault coverage purposes. Additionally, the designer does not need to manage inventories of different ASIC designs; FLEX 10K devices can be configured on the board for the specific functionality required.



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**EPM7128SLC84-15N**

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