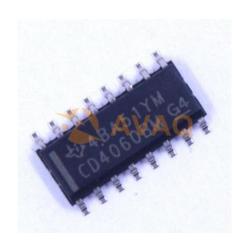


CD4060BM

Counter/Divider Single 14-Bit Binary UP 16-Pin SOIC Tube

Manufacturer:	Texas Instruments, Inc.
Package/Case:	SOP
Product Type:	Logic ICs
RoHS:	RoHS Compliant/Lead free W
Lifecycle:	Active





General Description

CD4060B consists of an oscillator section and 14 ripple-carry binary counter stages. The oscillator configuration allows design of either RC or crystal oscillator circuits. A RESET input is provided which resets the counter to the all-O's state and disables the oscillator. A high level on the RESET line accomplishes the reset function. All counter stages are master-slave flip-flops. The state of the counter is advanced one step in binary order on the negative transition of O). All inputs and outputs are fully buffered. Schmitt trigger action on the input-pulse line permits unlimited input-pulserise and fall times. The CD4060B-series types are supplied in 16-lead hermetic dual-in-line ceramic packages (F3A suffix), 16-lead dual-in-line plastic packages (E suffix), 16-lead small-outline packages (M, M96, MT and NSR suffixes), and 16-lead thin shrink small-outline packages (PW and PWR suffixes).

Key Features

12 MHz clock rate at 15 V

Common reset

Fully static operation

Buffered inputs and outputs

Schmitt trigger input-pulse line

100% tested for quiescent current at 20 V

Standardized, symmetrical output characteristics

5-V, 10-V, and 15-V parametric ratings

Meets all requirements of JEDEC Tentative Standard No. 13B, "Standard Specifications for Description of 'B' Series CMOS Devices'

Oscillator : All active components on chip

RC or crystal oscillator configuration

RC oscillator frequency of 690 kHz min. at 15 V

Applications Control counters

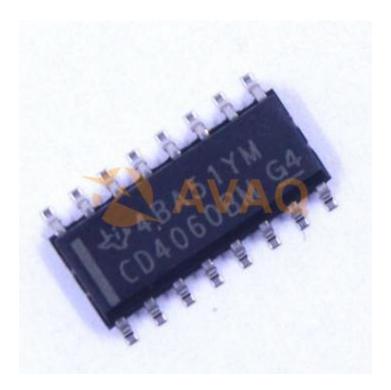
Timers

Frequency dividers

Time-delay circuits

Description

CD4060B consists of an oscillator section and 14 ripple-carry binary counter stages. The oscillator configuration allows design of either RC or crystal oscillator circuits. A RESET input is provided which resets the counter to the all-O's state and disables the oscillator. A high level on the RESET line accomplishes the reset function. All counter stages are master-slave flip-flops. The state of the counter is advanced one step in binary order on the negative transition of O). All inputs and outputs are fully buffered. Schmitt trigger action on the input-pulse line permits unlimited input-pulserise and fall times. The CD4060B-series types are supplied in 16-lead hermetic dual-in-line ceramic packages (F3A suffix), 16-lead dual-in-line plastic packages (E suffix), 16-lead small-outline packages (M, M96, MT and NSR suffixes), and 16-lead thin shrink small-outline packages (PW and PWR suffixes).



Recommended For You

CD4017BE Texas Instruments, Inc DIP16

CD74AC161M Texas Instruments, Inc SOP16

CD4026BE

Texas Instruments, Inc

CD4020BE

Texas Instruments, Inc DIP16

CD4510BNSR Texas Instruments, Inc SOP16

CD40193BE Texas Instruments, Inc

DIP CD4520BE

Texas Instruments, Inc DIP16

CD4516BE Texas Instruments, Inc DIP16

CD40110BE Texas Instruments, Inc DIP

CD4022BE Texas Instruments, Inc DIP

CD4024BM

Texas Instruments, Inc SOP14

CD4040BE

Texas Instruments, Inc DIP16

CD4060BE

Texas Instruments, Inc DIP16

CD74HCT193E

Texas Instruments, Inc

CD74HC193E Texas Instruments, Inc DIP