

NVRAM NVSRAM Parallel 4Mbit 5V 32-Pin EDIP

Manufacturer: [Maxim Integrated](#)

Package/Case: DIP

Product Type: Memory

Lifecycle: Obsolete



Images are for reference only

[Inquiry](#)

General Description

The DS1250 4096k Nonvolatile SRAMs are 4,194,304-bit, fully static, nonvolatile SRAMs organized as 524,288 words by 8 bits. Each complete NV SRAM has a self-contained lithium energy source and control circuitry which constantly monitors VCC for an out-of-tolerance condition. When such a condition occurs, the lithium energy source is automatically switched on and write protection is unconditionally enabled to prevent data corruption. DIP-package DS1250 devices can be used in place of existing 512k x 8 static RAMs directly conforming to the popular byte-wide 32-pin DIP standard. DS1250 devices in the PowerCap Module package are directly surface mountable and are normally paired with a DS9034PC PowerCap to form a complete Nonvolatile SRAM module. There is no limit on the number of write cycles that can be executed and no additional support circuitry is required for microprocessor interfacing.

Key Features

CC

CC



Recommended For You

DS1220AB-150+

Maxim Integrated

DIP-24

DS1245Y-70+

Maxim Integrated

DIP32

DS1245Y-100+

Maxim Integrated

DIP

DS1250Y-70

Maxim Integrated

DIP

DS1220AB-200+

Maxim Integrated

24-EDIP

DS1212

Maxim Integrated

DIP

DS1230Y-70+

Maxim Integrated

DIP

DS1250Y-70IND+

Maxim Integrated

DIP-32

DS1230W-150+

Maxim Integrated

DIP-28

DS1312+

Maxim Integrated

SMDSMT

DS1321+

Maxim Integrated

DIP-16

DS1212+

Maxim Integrated

BGA

DS1314S+

Maxim Integrated

SOP16

DS9092

Maxim Integrated

IBACC

DS9092L

Maxim Integrated

IBACC